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Washington, D.C. 20231

Date: December 11, 2009

Patent No.: : 7,576,619 B2
Application No.: 10/520,805
Issued : August 18, 2009
Inventor : Kehrner, et al.
Title : **INTEGRATED CIRCUIT ARRANGEMENT**

Re: Request for Certificate of Correction

Consideration has been given your request for the issuance of a certificate of correction, for the above-identified patent under the provision of Rule 1.322 or 1.323.

Respecting the alleged error, on the title page there is no certified copy of the priority document /DM/. Therefore, no correction(s) is in order here under United States Codes (U.S.C.) 254 or 255 and the Code of Federal Regulation (C.F.R.) 1.322 or 1.323.

In view of the foregoing, your request is hereby denied.

Further correspondence concerning this matter should be filed and directed to Decisions and Certificates of Correction Branch. Any response must be filed within a four week period.

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